



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OHASHI, Hitoshi et al.**

Group Art Unit: 1792

Serial No.: 10/047,992

Examiner: **TALBOT, Brian K.**

Filed: **January 17, 2002**

P.T.O. Confirmation No.: 5363

For. **METHOD AND APPARATUS FOR FORMING ELECTRIC CIRCUIT FIXING  
THE SEMICONDUCTOR DEVICE USING SUBSTRATE JIG**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: June 9, 2008

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated January 8, 2008 for two-months, from April 8, 2008 to June 8, 2008.

Please charge our Deposit Account No. 01-2340 in the amount of \$460.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,  
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DC/llf

Atty. Docket No. **020052**  
Suite 400  
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PATENT & TRADEMARK OFFICE

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